

Contact

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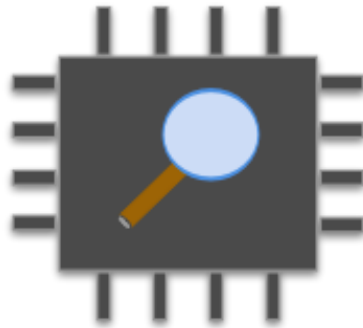
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IBIS DRC

Signal Integrity IBIS Simulator Design Rule Checker

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IBIS DRC allows engineers an easy way to validate IC vendor IBIS models for Signal Integrity simulation and analysis. Run over 75+ custom design rules and get an HTML generated quality report instantly. The built in quality checker allows an engineer to compare the actual IBIS model to the device datasheet to validate the quality of the model. The advanced GUI editor allows for easily updating and fixing IBIS model issues.

IBIS models can be simulated into various topologies with supported simulators including Synopsys HSPICE.

With PCB designs becoming more complex and increasing bus speeds the need for simulation is even greater. Engineers need to validate IC vendor IBIS models before simulation to ensure accurate results. A study showed **59% of IC vendor IBIS models are not accurate** for simulation without modifications.

IBIS DRC provides automatic IBIS model quality validation for Signal Integrity design analysis.

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